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**NEC**

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ICT

TCAM 2

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DRAM CPU

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1.           CAM          

CAM

2

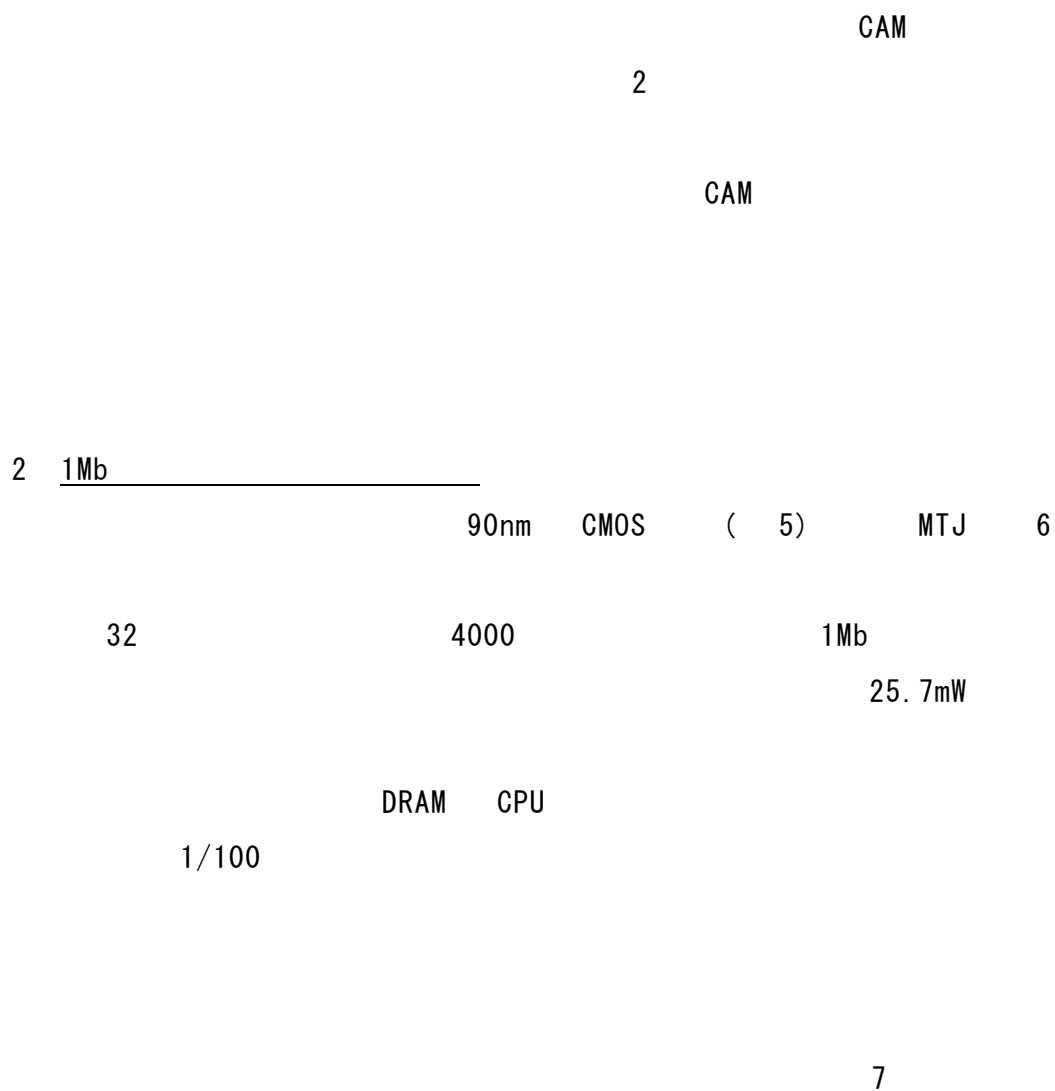
CAM

DRAM CPU

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CAM

CAM



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 Symposium on VLSI Circuits (VLSI

Circuits) 2013 13

1  
2 CAM Content-Addressable Memory TCAM Ternary CAM  
CAM  
3 [http://jpn.nec.com/press/201302/20130219\\_02.html](http://jpn.nec.com/press/201302/20130219_02.html)  
4  
5 CMOS N P  
6 MTJ:Magnetic Tunnel Junction:  
7

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